

GP/2635 #3 BT
02-25-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Patent & Trademark Office et al.

Examiner: UNASSIGNED

Serial No.: 09/932,061

Group Art Unit: 2635

Filed: August 17, 2001

Docket: 50019.77US01/P05030

Due Date: November 17, 2001

Title: APPARATUS AND METHOD FOR DETECTING A FAULT CONDITION IN A COMMON MODE SIGNAL

RECEIVED

FEB 05 2002

Technology Center

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the paper(s), as described herein, are being deposited in the United States Postal Service, as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on November 16, 2001.

By: Fred A. Kaseburg
Fred A. Kaseburg

Commissioner for Patents
Washington, D.C. 20231

Sir:

We are transmitting herewith the attached:

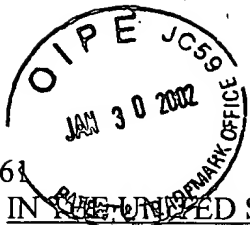
- ☒ Transmittal Sheet in duplicate containing Certificate of Mailing
- ☒ Information Disclosure Statement, Form 1449, Six (6) Reference(s)
- ☒ Return postcard

Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers or any future reply, if appropriate. Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. A duplicate of this sheet is enclosed.

MERCHANT & GOULD P.C.
P.O. Box 2903, Minneapolis, MN 55402-0903
206.342.6200

By: Fred A. Kaseburg
Name: Fred A. Kaseburg
Reg. No.: 47,695
FAK/aj





S/N 09/932,061

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Hannan et al.	Examiner:	UNASSIGNED
Serial No.:	09/932,061	Group Art Unit:	2635
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By:

Fred A. Kaseburg

INFORMATION DISCLOSURE STATEMENT (37 C.F.R. § 1.97(b))

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

With regard to the above-identified application, the items of information listed on the enclosed Form 1449 are brought to the attention of the Examiner.

This statement should be considered because it is submitted within three months of the filing date of the above-identified application, which is not an application under 37 C.F.R. § 1.53(d). Accordingly, no fee is due for consideration of the items listed on the enclosed Form 1449.

In accordance with 37 C.F.R. § 1.98(a)(2), a copy of each document or other information listed on the enclosed Form 1449 is provided.

No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103 and Applicants reserve the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish that the reference(s) are not "prior art." Moreover, Applicants do not


represent that a reference has been thoroughly reviewed or that any relevance of any portion of a reference is intended.

Consideration of the items listed is respectfully requested. Pursuant to the provisions of M.P.E.P. 609, it is requested that the Examiner return a copy of the attached Form 1449, marked as being considered and initialed by the Examiner, to the undersigned with the next official communication.

Please charge any additional fees or credit any overpayment to Deposit Account No. 13-2725.

Respectfully submitted,

MERCHANT & GOULD


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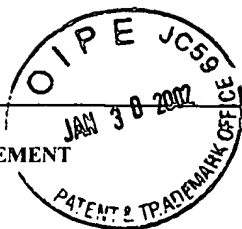


FORM 1449*

INFORMATION DISCLOSURE STATEMENT

IN AN APPLICATION

(Use several sheets if necessary)

Docket Number:
50019.0077 US 01Application Number:
09/932,061

Applicant: Douglas Michael Hannan and Roy L. Harbrough

Filing Date: August 17, 2001

Group Art Unit: 2635

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FEB 05 2002

Technology Center 2600

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	US 6,252,435 B1	06/26/2001	Wu et al.	327	65	10/05/2000

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	National Semiconductor Corporation, Syed B. Huq/John Goldie, "An Overview of LVDS Technology," July 1998 (6 pages)
	National Semiconductor Corporation, "LVDS Owner's Manual, Introduction to LVDS," 2000 (16 pages)
	"IEEE Standard Dictionary of Electrical and Electronic Terms," 6 th Edition 1996, page 388, "fail-safe circuit" (1 page)
	Texas Instruments, "Low Voltage Differential Signaling (LVDS) Technology: The Basics," 2000 (3 pages)
	Texas Instruments, "Industry's First LVDS Dual Driver/Receiver Devices Extend Reach of New Data Interface Standard," 2000 (4 pages)



EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.